

Datasheet Brief KTS1656

For full datasheet, click HERE.

The KTS1656 features two low resistance power switches

configured as single input, dual output, change-over

switch. The input to both switches is protected against

VBUS surge voltages of up to ±200V, and is also protected

against over-voltage, with preset trip points on both the VBUS-to-VOUT and VBUS-to-SYS paths, providing

protection to downstream components from abnormal input

The main switch (VBUS-to-VOUT) is an active-LOW

enabled, reverse-blocking 3.5A rated MOSFET, with an

OVP trip point of 13.9V. The secondary switch (VBUS-to-

SYS) is an active-HIGH enabled, reverse-blocking, 6.0A

rated MOSFET, with an OVP trip point of 5.25V. The VBUS

When VBUS is greater than 2.5V typ, the LDO output

provides an "always ON" power source, regardless of the

OVLO, EN1 and EN2 state, to power downstream

components, thereby permitting operation without an

The KTS1656 also features an active-HIGH SHDN pin to

conserve power and an over-temperature thermal

The KTS1656 is packaged in advanced, fully "green"

compliant, 2.71 x 3.01mm, Wafer-Level Chip-Scale

Surge Protected, Single Input, Dual Output Load Switch with OVP

conditions.

installed battery.

Package (WLCSP).

Applications

Smartphones and Tablets

• Wearables and other Portable Devices

Mobile Internet Devices

protection.

Brief Description

input is rated from -6V to 28V.

Features

- Single Input, Dual Output Low-Ron Switch
 - ► VBUS to VOUT: 20mΩ typ
 - VBUS to SYS: 35mΩ typ
 - ► Reverse Blocking on Both Switch Paths
- Wide Input Voltage Range: 2.7V 13.2V
- ► VBUS Abs Max: -6V to 28V
- Surge Protected VBUS
 - ▶ IEC61000-4-5: > ±200V
- ESD Protection
 - ► IEC61000-4-2 (Level 4) VBUS
 - Contact: ±8kV
 - Air Gap: ±15kV
 - ► HBM: 2kV All Pins
- Integrated Over-voltage Protection (OVP)
 - ▶ VBUS to VOUT: 13.9V
 - ► VBUS to SYS: 5.25V
- Maximum Continuous Current
 - ► VBUS to VOUT: 3.5A
 - VBUS to SYS: 6A
- OTG Compatible Power-Up
- Dual Enable Control with Independent Shutdown Control
 - Active LOW VBUS to VOUT
 - Active HIGH VBUS to SYS
- Active HIGH Shutdown
- VBUS detection LDO
- VBUS to SYS FLAG
- VBUS Active Discharge Control Input
- Over Temperature Protection
- Pb-free 42-Bump, WLCSP 2.71mm x 3.01mm
- -40°C to 85°C Operating Temperature Range

Typical Application

Battery Charger Adapter/USB VBUS VOUT CHGIN SYS VIN \sim COUT CVBUS 1μF BAT 1uF GND SYS C_{SYS} + Li Ion System 4.7µF **KTS1656** Batterv PMIC SHDN EN1 EN2 ╧ FLAG VBUSD LDO CLDO 2.2uF (Up to 20mA)



Ordering Information

Part Number	Marking ¹	Operating Temperature	Package
KTS1656EUY-TR	LAXXYYZZZZ	-40°C to +85°C	WLCSP42

1. "LAXXYYZZZZ" is the device ID code, date code, assembly code and serial number.

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